

NO. 494 P. 1/5

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FACSIMILE COVER SHEET

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
MEARS ET AL.

Serial No. 10/716,783

Filing Date: NOVEMBER 19, 2003

For: METHOD FOR MAKING
SEMICONDUCTOR DEVICE
INCLUDING BAND-ENGINEERED
SUPERLATTICE

Attorney Docket No. 62603_CON1

REQUEST FOR CORRECTION TO THE FILING RECEIPT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The filing receipt for the subject patent application has been received and reviewed for accuracy. Two discrepancies have been discovered. The filing receipt in error lists the name of the Assignee as "R.J. Mears LLC". The correct name of the Assignee is RJ Mears, LLC. The next error occurs because the address of the Assignee is not listed. The new address of the Assignee is Waltham, MA.

In view of the errors noted, it is respectfully requested that a corrected filing receipt be issued in this application. If any fee is believed to be necessary, authorization is given to charge Deposit Account No. 01-0484.

Respectfully submitted,

CHRISTOPHER F. REGAN

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Attorney for Applicants

In re Patent Application of:
MEARS ET AL.
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CERTIFICATE OF FACSIMILE

I HEREBY CERTIFY that this REQUEST FOR CORRECTION TO
THE FILING RECEIPT for U.S. Serial No. 10/716,783 is being
submitted via telefacsimile no. 703-746-9195 to: OFFICE OF
INITIAL PATENT EXAMINATION'S FILING RECEIPT CORRECTIONS, P.O. BOX
1450 ALEXANDRIA, VA 22313-1450, this 22nd day of March, 2004.

Dawn K. Komer

**** SMALL ENTITY ******Title**

Method for making semiconductor device including band-engineered superlattice

Preliminary Class

438

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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